

MEMORY SYSTEMS

Fig 1: Driver's Memory Seat Circuit (1 of 2)

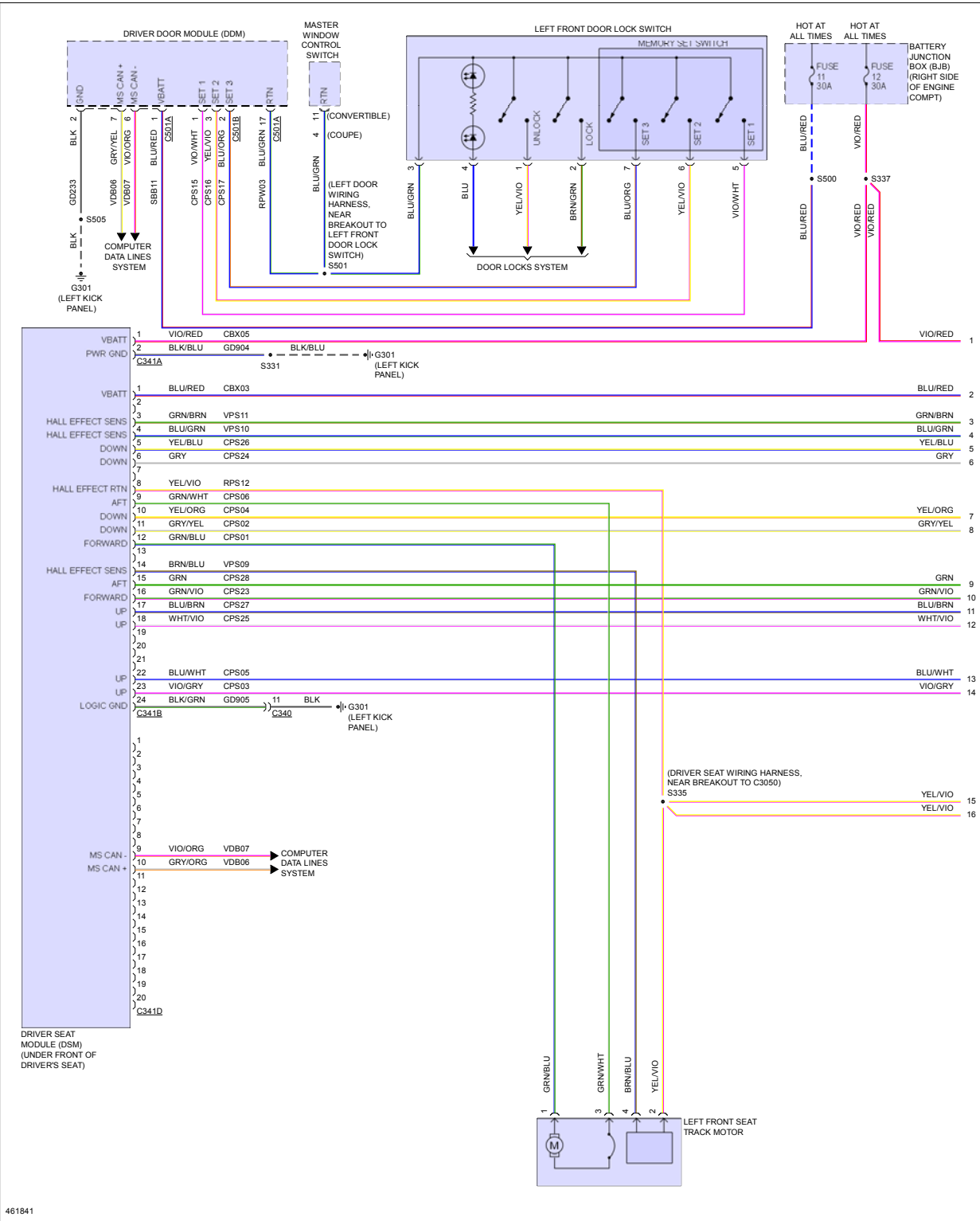


Fig 2: Driver's Memory Seat Circuit (2 of 2)

